



**K. J. Somaiya College of Engineering, Mumbai-77**  
(Autonomous College Affiliated to University of Mumbai)

**M.E. (SEM. I & II) ESE THEORY EXAMINATION TIME TABLE**  
(MU NON- CBGS Syllabus Under Autonomy)  
**{For students admitted before July 2012}**

**FIRST HALF - 2019**

Candidates appearing for the above examinations are requested to be present at the place of examination, **fifteen minutes** before the commencement of the examinations and will not be allowed after the half an hour of the commencement of the examinations. The candidates are required to carry the hall ticket / college Identity Card. **THEY ARE FORBIDDEN TO CARRY ANY ELECTRONICS GADGETS LIKE MOBILE PHONE, SMART WATCH, ETC., ANY BOOKS, NOTES OR PAPER ETC. IN THE EXAMINATION HALL.** Seat numbers will be displayed on the college notice boards before the date of commencement of the examination and seating arrangement will be displayed on college notice boards on the day of examination. The ESE examinations of **M.E. (SEM – I & II)** will be conducted as per the following schedule: -

DAY	FRIDAY	MONDAY	WEDNESDAY	FRIDAY	MONDAY
DATE	<b>17 May 2019</b>	<b>20 May 2019</b>	<b>22 May 2019</b>	<b>24 May 2019</b>	<b>27 May 2019</b>
TIME	10.30AM– 1.30 PM	10.30AM– 1.30 PM	10.30AM– 1.30 PM	10.30AM– 1.30 PM	10.30AM– 1.30 PM
ELECTRONICS ENGINEERING	Wireless and Mobile Network <i>equivalent for Advanced Communication Theory of semester II</i>	Embedded System <i>equivalent for VLSI Design of semester II</i>	Power Electronic Devices and Design <i>equivalent for Power Electronics of semester II</i>	Advance Processor Architecture <i>equivalent for Microprocessor and System II of semester II</i>	Instrumentation System Design <i>equivalent for Process Instrumentation and controller design of semester II</i>

Candidates are permitted to use the scientific but not programmable calculator at the examination. Candidates for the examination are expected to provide themselves with Data books and drawing instruments if required. The exchange or loan of Data books, drawing instruments or any other materials used in the examination is not permitted while the examination is in progress. Candidates must bring their own instruments and will not be allowed to borrow from each other under any circumstances. Any unfair means in the examination hall during the theory examination is punishable as per the rules and regulations

Mumbai – 400 077  
DATE: 8 April 2019

389  
12/04/19  
PRINCIPAL





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FIRST HALF - 2019

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DAY	WEDNESDAY	FRIDAY	MONDAY	THURSDAY	SATURDAY
DATE	29 May 2019	31 May 2019	3 June 2019	6 June 2019	8 June 2019
TIME	10.30 AM - 1.30 PM	10.30 AM - 1.30 PM	10.30 AM - 1.30 PM	10.30 AM - 1.30 PM	10.30 AM - 1.30 PM
ELECTRONICS ENGINEERING	Advanced Digital Image Processing <i>equivalent for Image Processing of semester I</i>	Application Specific IC Design <i>equivalent for Digital System Design of semester I</i>	Advanced Digital Communication <i>equivalent for Communication Theory of semester I</i>	Digital System Design <i>equivalent for Microprocessor and system I of semester I</i>	Applications of DSP and IP <i>equivalent for Discrete time Signal Processing and Applications of semester I</i>

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